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Title of Invention	Lateral Transistor Having Graded Base Region, Semiconductor Integrated Circuit And Fabrication Method Thereof				
Named Inventor(s)	Makoto Yamamoto; Akio Iwabuchi	494			
Attorney Docket	44471-265522 (13700)	22			
Express Mail Label No.	EL910717824US	09/0			

(Only for new nonprovisional applications under 37 CFR 1.53(b))

ADDITION ELEMENTO	Assistant Commissioner for Patents ADDRESS TO:  Box Patent Application			
APPLICATION ELEMENTS	Washington, D.C. 20231			
	ACCOMPANYING APPLICATION PARTS			
<ol> <li>Fee Transmittal Form         (Submit an original, and a duplicate for fee processing)</li> <li>Applicant claims Small Entity status</li> <li>Specification, Claims,         and Abstract Total Pages 23</li> <li>Drawings Total Sheets 9         Total Pages 32</li> </ol>	7. Nucleotide and/or Amino Acid Sequence Submission (if applicable, all necessary) a. Computer Readable Copy b. Paper Copy (identical to computer copy) c. Statement verifying identity of above copies			
	8. Assignment:			
<ul> <li>5. Oath or Declaration <ul> <li>a. Newly executed (original or copy)</li> <li>b. Copy from prior application (37 CFR 1.63(d)) <ul> <li>(for continuation/divisional with Box 16 completed)</li> <li>The entire disclosure of the prior application, from with a copy of the oath or declaration is supplied under B 5b, is considered as being part of the disclosure of the accompanying application and is hereby incorporate by reference therein. <ul> <li>(i) DELETION OF INVENTOR(S)</li> <li>Signed statement attached deleting inventor(s) named in the prior application, see 37 CFR 1.63(d)(2) and 1.33(b).</li> </ul> </li> <li>6. Microfiche Computer Program (Appendix)</li> </ul></li></ul></li></ul>	Power of Attorney by assignee  10. English Translation Document (if applicable)  11. Information Disclosure Statement (IDS) PTO- 1449			
16. If a CONTINUING APPLICATION, check appropria				
Continuation Divisional Continua  Recite complete dependency back to first parent application	ation-in-part (CIP) of prior application No:			
	PONDENCE ADDRESS			
☑ Customer Number or Bar Code Label  23370 (Insert Customer Number Open Customer National Abandon Aba				
Name (Print/Type) Roger T. Frost	Registration No. (Attorney/Agent) 22,176			
Telephone (404)815-6500				
Signature / Min F. From	Date S. 26, 2001 October 26, 2001			

## FEE TRANSMITTAL

Attorney Docket No. 4771-265522 (13700)

This sheet accompanies a patent application transmittal for the following application:

Inventor(s):

Makoto Yamamoto, et al.

Filing Date:

October 26, 2001

Title:

Lateral Transistor Having Graded Base Region, Semiconductor

**Integrated Circuit And Fabrication Method Thereof** 

The filing fee is calculated as shown below:

1. FILING FEE:

SMALL ENTITY

LARGE ENTITY

FOR:	FEE	FEE PAID	FEE	FEE PAID
UTILITY FILING FEE	\$370	\$	\$740	\$740
DESIGN FILING FEE	\$165		\$330	
PLANT FILING FEE	\$255		\$510	
REISSUE FILING FEE	\$370		\$740	
PROVISIONAL FILING FEE	\$80		\$160	
	SUBTOTAL (1)	\$		\$740

## 2. ¡≟CLAIMS:

**SMALL ENTITY** 

LARGE ENTITY

FOR:	NO. FILED	NO. EXTRA	RATE	FEE	RATE	FEE
TOFAL CLAIMS	19 - 20 =	0	x 9 =	\$	x 18 =	\$0
INDEP. CLAIMS	3 - 3 =	0	x 42 =		x 84 =	0
"_MULTIPLE DEPENDENT CLAIM PRESENTED		+140 =		+280 =		
		SUB	TOTAL (2)	\$		\$0

3. ADDITIONAL FEES:

SMALL ENTITY

LARGE ENTITY

FOR:	FEE	FEE PAID	FEE	FEE PAID
LATE FILING, FEE OR OATH	\$65	\$	\$130	\$
NON-ENGLISH SPECIFICATION	\$130		\$130	
OTHER				
	SUBTOTAL (3)	\$		\$

TOTAL FILING FEES: \$740

A check is enclosed for the total amount: \$740

Charge any additional fees required under 37 C.F.R. 1.16 or 1.17 to Deposit Account 11-0855.

John S. Pratt

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Reg. No. 22,176

Date: October 26, 2001